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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Complete if Known

Application Number	10/771,611
Filing Date	February 4, 2004
First Named Inventor	Gomm, Tyler
Group Art Unit	2816
Examiner Name	Cox, Cassandra

Sheet 1 of 1

Attorney Docket No: 303.816US2

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
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CE	US-6,172,537	01/09/2001	Kanou, H., et al.	04/06/1999
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
CE		TAMURA, H. , et al., "Partial response detection technique for driver power reduction in high speed memory-to-processor communications", <u>IEEE International Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC., (1997),342-343, 482</u>	

EXAMINER

Cassandra Cox

DATE CONSIDERED

April 1, 2006